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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

30879 U.S. PTO  
10/046805

APPL NUM 10046805	FILING DATE 01/14/2002	CLASS 438	SUBCLASS 674	GAU 2842	EXAMINER 2829 Kilday
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**\*\*APPLICANTS:** Lee Myo-Jong; Choi Seung-Man; Kang Sang-Bum; Choi Gil-Heyun;

class 27-30 → class 257/734+  
→ claims 1-27 and 31-62  
class 438/674

**\*\*CONTINUING DATA VERIFIED:**  
18K none 6/26/03

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
18K none 6/26/03

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no late 6/26/03	ATTORNEY DOCKET NO 5649-968
TITLE : Methods of forming metal layers in integrated circuit devices using selective deposition on edges of recesses and conductive contacts so formed		

U.S. DEPT. OF COMM/PAT & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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